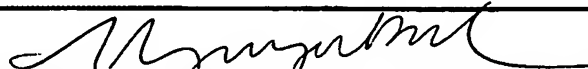


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		Filing Date	March 30, 2004
		First Named Inventor	Deepak Sabharwal et al.
		Art Unit	
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Sheet 1	of 1	Attorney Docket Number	1263-0025US

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DN		KENICHI OSADA et al.; "16.7fA/cell Tunnel-Leakage-Suppressed 16MB SRAM for Handling Cosmic-Ray-Induced Multi-Errors"; 2003 IEEE International Solid-State Circuits Conference; Pages 302-303.	

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